

CLAIMS

The following claims are pending:

1. (Previously Presented) A method for coding a hardware description of a peripheral device for multiple instantiations within a single chip, the method comprising:
 - configuring a function block to instantiate the hardware description with options associated with different configurations of the peripheral device;
 - selecting between the options at compile time for each instantiation of the peripheral device, wherein the options are selected without modification to the hardware description; and
 - compiling the hardware description to produce a structural model comprising each instantiation of the peripheral device with the selected options of the different configurations for that instantiation.
2. (Original) The method of claim 1 wherein the step of selecting comprises:
 - passing a parameter value to the function block at compile time for each instantiation of the hardware peripheral; and
 - instantiating the peripheral device using code according to the parameter value.
3. (Original) The method of claim 1 wherein the configuration options are peripheral design functions, peripheral design pin widths, or peripheral design interface pin outs.
4. (Original) The method of claim 1 and further comprising:
 - tying strap pins to power or ground.
5. (Original) The method of claim 1 wherein the step of configuring comprises:
 - configuring the function block with local runtime constants adapted to be overridden individually at compile time.

6. (Original) The method of claim 5 wherein the step of selecting comprises overriding selected runtime constants at compile time to select between the variable options for each instance of the peripheral device.
7. (Previously Presented) A method for coding a reusable hardware description of a peripheral device for multiple instantiations within an integrated circuit, the method comprising:
 - configuring a function block to instantiate the reusable hardware description with options at compile time;
 - instantiating multiple instances of the peripheral device on the integrated circuit by programmatically selecting between the options at compile time for each instantiation of the peripheral device; and
 - compiling the reusable hardware description to produce a structural model comprising the multiple instantiations of the peripheral device, each with the selected options for that instantiation.
8. (Original) The method of claim 7 wherein the variable options are selected without modification to the reusable hardware description.
9. (Original) The method of claim 7 wherein the step of configuring comprises:
 - adding one or more peripheral devices based on desired features of the reusable hardware to the integrated circuit at compile time.
10. (Original) The method of claim 7 wherein the step of configuring comprises:
 - instantiating peripheral devices onto the integrated circuit according to the reusable hardware description wherein each instantiation is unique based on a design parameter.

11. (Original) The method of claim 10 wherein the design parameter comprises a signal width of the peripheral device.
12. (Original) The method of claim 7 and further comprising:
defining further the function block by tying strap pins to ground or to power.
13. (Original) The method of claim 7 wherein the step of configuring further comprises:
configuring the function block with parameters local in scope, the parameters adapted to be overridden individually at compile time.
14. (Original) The method of claim 13 wherein the step of selecting comprises
overriding selected runtime constants at compile time to select between the options for each instance of the peripheral device.
15. (Original) The method of claim 7 wherein the step of configuring comprises:
passing a parameter value to the function block at compile time for each instantiation of the peripheral device; and
instantiating the peripheral device using the reusable hardware description according to the parameter value.
16. (Previously Presented) A method for instantiating multiple instances of a peripheral device within an integrated circuit design, the method comprising:
configuring a hardware description block to describe the peripheral device and to describe options associated with different configurations of the peripheral device; and
selecting between the options at compile time for each instantiation of the peripheral device without modifying the hardware description block; and
compiling the hardware description to produce a structural model comprising each

instantiation of the peripheral device with the selected options of the different configurations for that instantiation.

17. (Original) The method of claim 16 wherein the step of selecting comprises:
passing a parameter value to the function block at compile time for each
instantiation of the hardware peripheral; and
instantiating the peripheral device with options determined by the parameter value.
18. (Original) The method of claim 16 wherein the step of configuring comprises:
coding the hardware description block with local runtime constants adapted to be
overridden individually at compile time.
19. (Original) The method of claim 16 wherein the variable options comprise local runtime constants and wherein the step of selecting comprises:
selecting one or more of the local runtime constants at compile time; and
overriding the selected one or more of the local runtime constants to differentiate
each instance of the peripheral device as needed.
20. (Original) The method of claim 16 wherein the options comprise peripheral device functions,
peripheral device pin widths or peripheral device signal widths.